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Filing Date	2003-12-08
First Named Inventor	Kushagra Vaid
Art Unit	2113
Examiner Name	Elmira Mehrmanesh
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EM	1	Quach, N., "High Availability and Reliability in the Itanium Processor," IEEE Micro, September-October 2000, pp. 61-69.	<input type="checkbox"/>
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Examiner Signature	/Elmira Mehrmanesh/	Date Considered	09/18/2006
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